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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number		10/672,961
				Filing Date		September 26, 2003
(use as many sheets as necessary)				First Named Inventor		Glenn J. Leedy
				Art Unit		2822
Sheet 1 of 1				Examiner Name		Monica Lewis
				Attorney Docket Number		ELM-2 Cont. 4

U.S. PATENT DOCUMENTS					
Examiner initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
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		Country Code ¹ - Number ² - Kind Code ³				
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NON PATENT LITERATURE DOCUMENTS			
Examiner initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
		Mitsumasa Koyanagi et al., "Design of 4-KBIT X 4-layer Optically Coupled Three-Dimensional Common Memory For Parallel Processor System," IEEE Journal of Solid-State Circuits, Vol. 25. No. 1, February 1, 1990.	